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	Application No.	Applicant(s)
	09/692,647	MCAFEE, MARTIN
Notice of Allowability	Examiner	Art Unit
	Scott T Baderman	2113
The MAILING DATE of this communication appear All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to	olication. If not included will be mailed in due course. THIS
1. This communication is responsive to the communication filed 16 September 2004.		
2. The allowed claim(s) is/are 23-30.		
3. A The drawings filed on 19 October 2000 are accepted by the Examiner.		
<ul> <li>4.</li></ul>		
Attachment(s)  1. Notice of References Cited (PTO-892)  2. Notice of Draftperson's Patent Drawing Review (PTO-948)  3. Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date  4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Summary Paper No./Mail Dat 8), 7. ☐ Examiner's Amendn	e

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## **DETAILED ACTION**

## Allowable Subject Matter

- 1. Claims 23-30 are allowed.
- 2. The following is an examiner's statement of reasons for allowance:

With respect to claim 23, the Examiner asserts that the novelty of the claim, when read as a whole, is wherein each switch includes "a second input to the switch coupled to the associated microprocessor socket and operable to receive a debugging signal transmitted through a microprocessor, if present, in the associated microprocessor socket, wherein a first input is passed by the switch and the second input is not passed by the switch if the logic signal of a third input indicates that the associated microprocessor socket is not populated by a microprocessor, and wherein the second input is passed by the switch and the first input is not passed by the switch if the logical signal of the third input indicates that the associated microprocessor socket is populated by a microprocessor."

With respect to claim 27, the Examiner asserts that the novelty of the claim, when read as a whole, are the steps receiving at the switch a second signal representing the debugging signal transmitted from the microprocessor socket, and passing a first signal and blocking the second signal if a third signal indicates that the microprocessor socket is not occupied by a

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microprocessor, and blocking the first signal and <u>passing the second signal</u> if the third signal that the microprocessor socket is occupied by a microprocessor."

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Scott T Baderman whose telephone number is (571) 272-3644. The examiner can normally be reached on Monday-Friday, 6:45 AM-4:15 PM, first Fridays off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Beausoliel can be reached on (571) 272-3645. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Scott T Baderman Primary Examiner Art Unit 2113

STB